

Figure 1 System Architecture Block Diagram

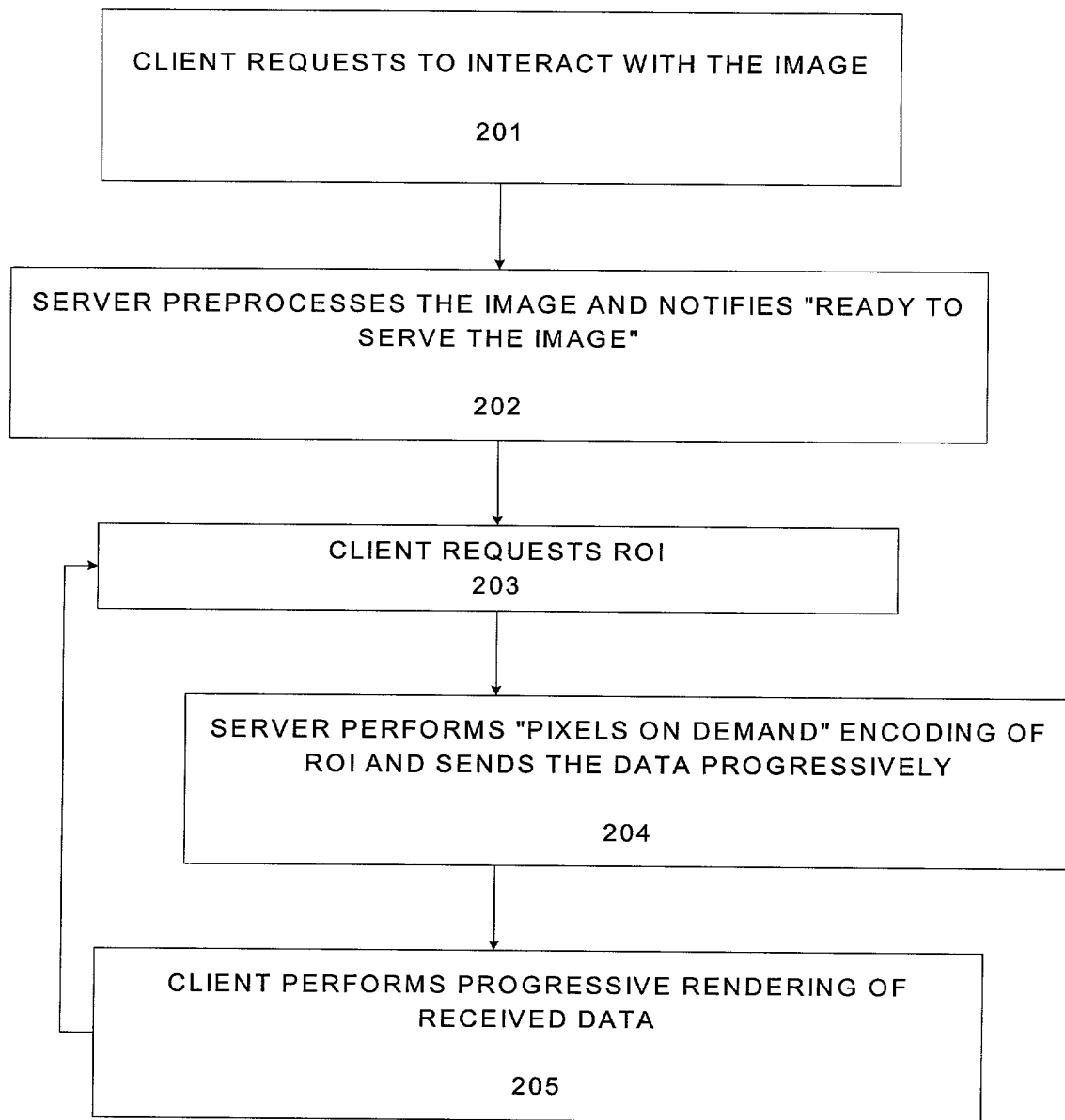


FIG. 2

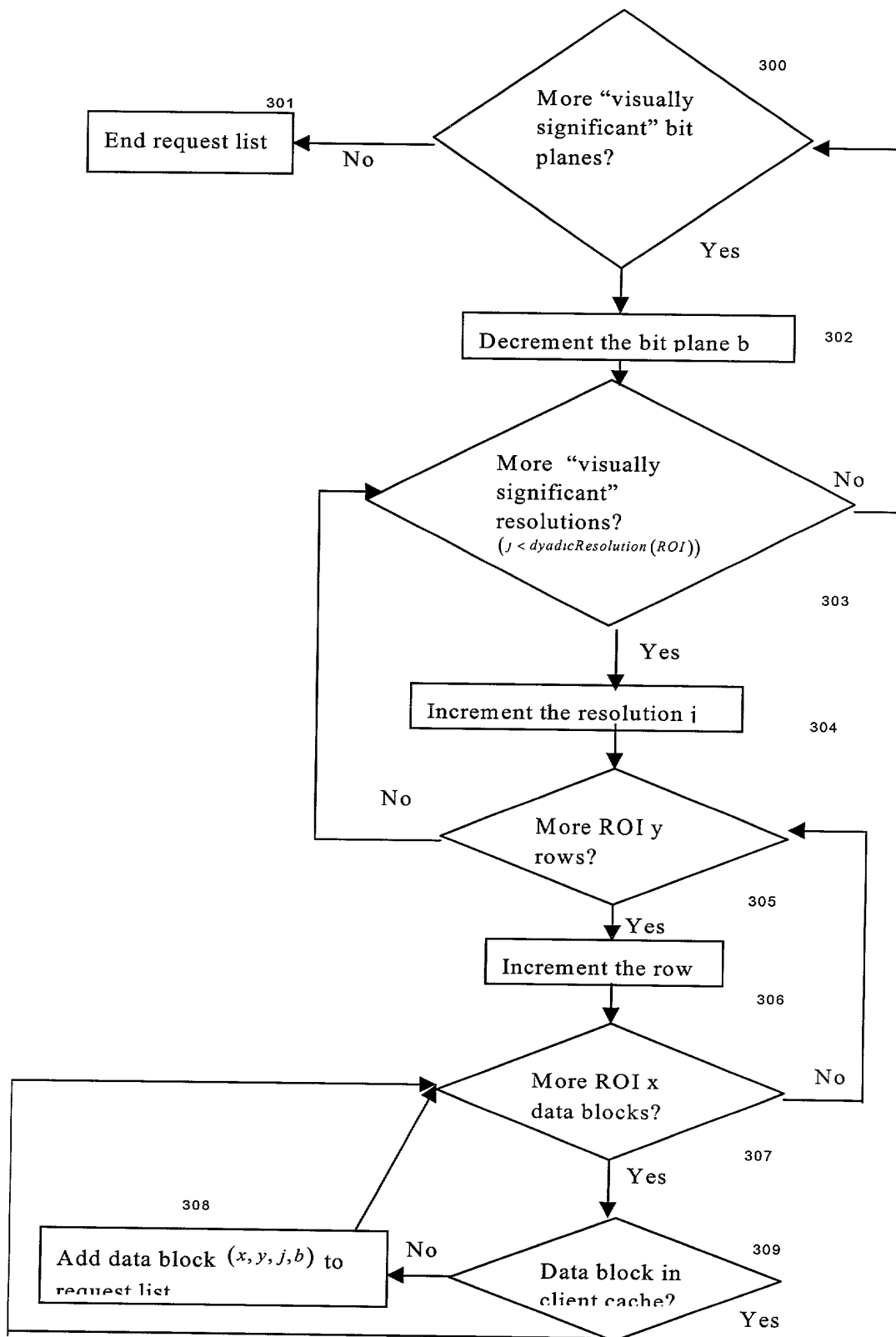


Figure 3

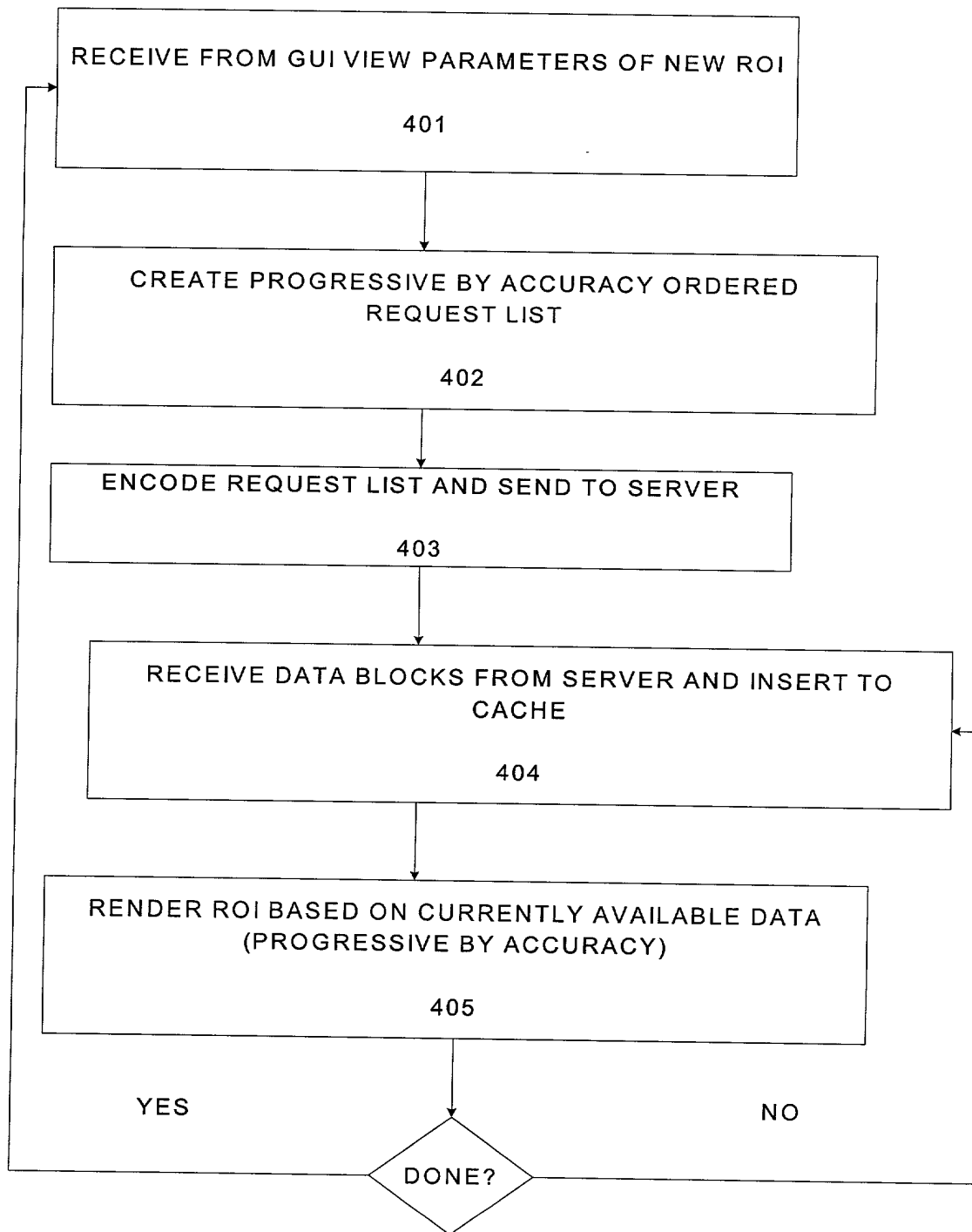


FIG. 4

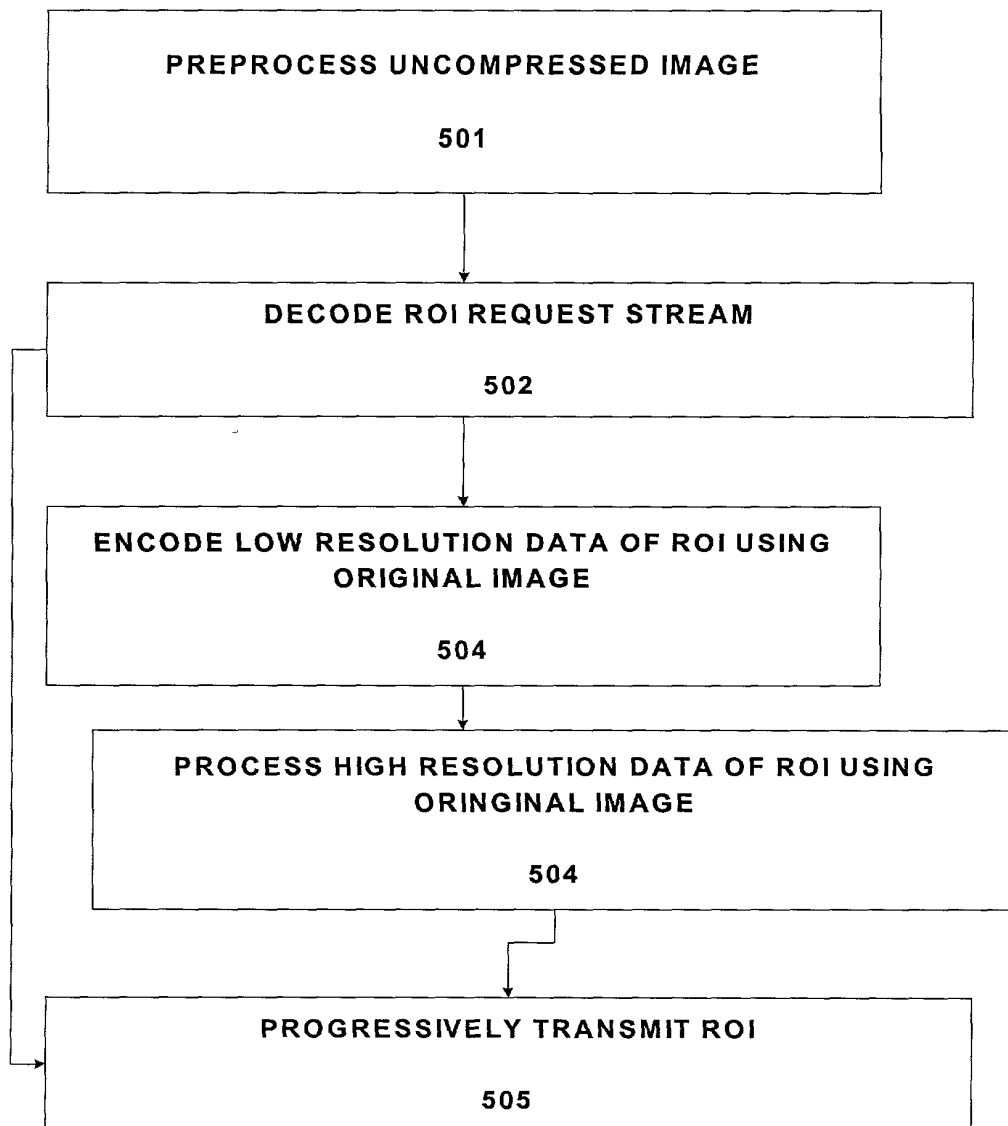


FIG. 5

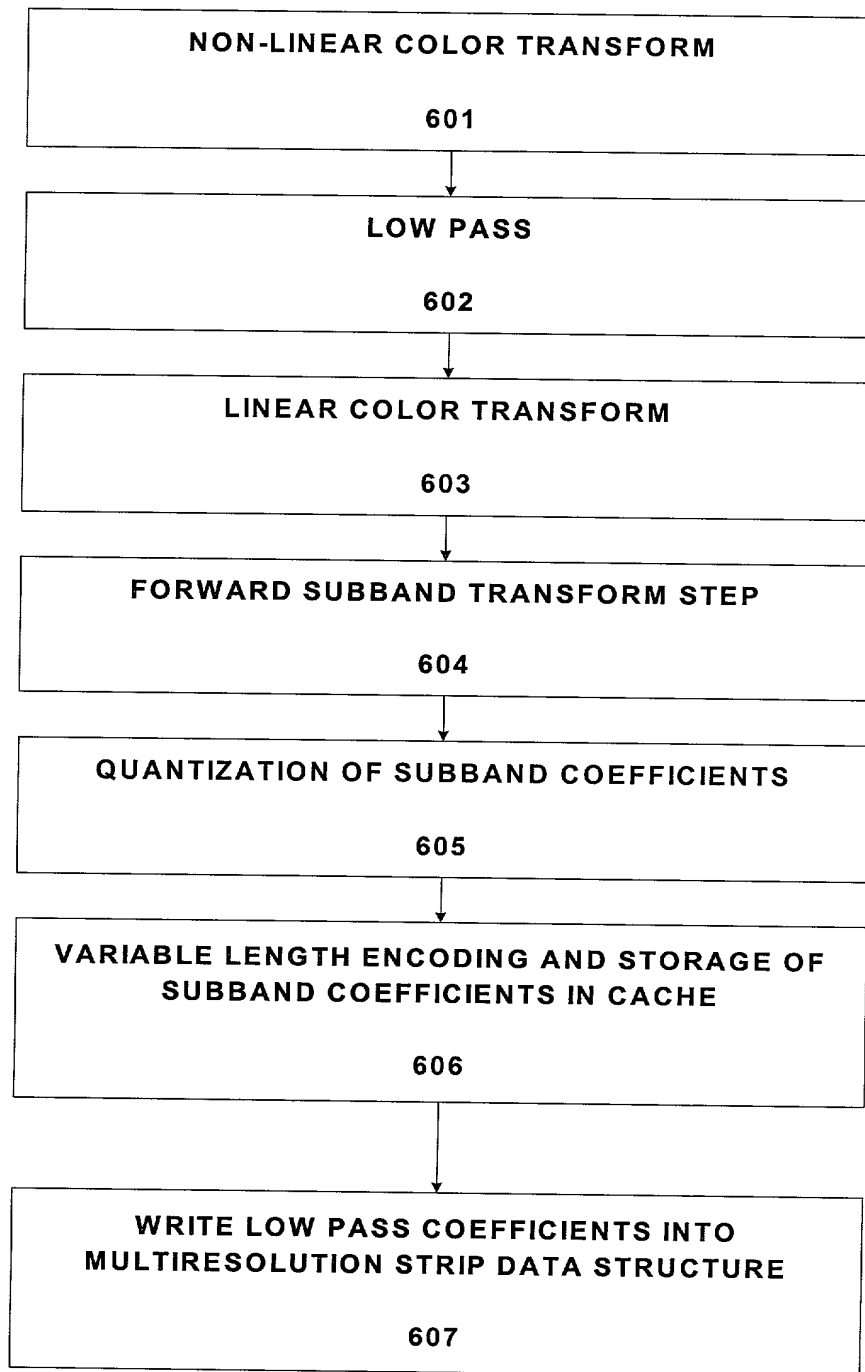


FIG. 6

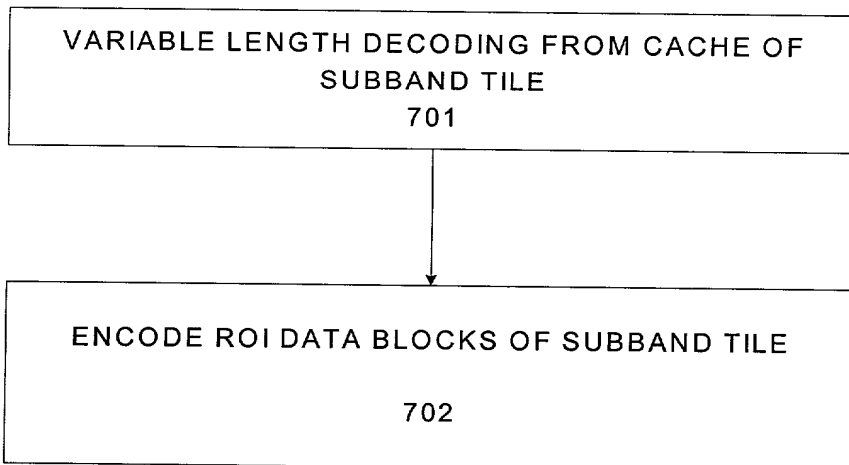


FIG. 7

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T02T0"29971860

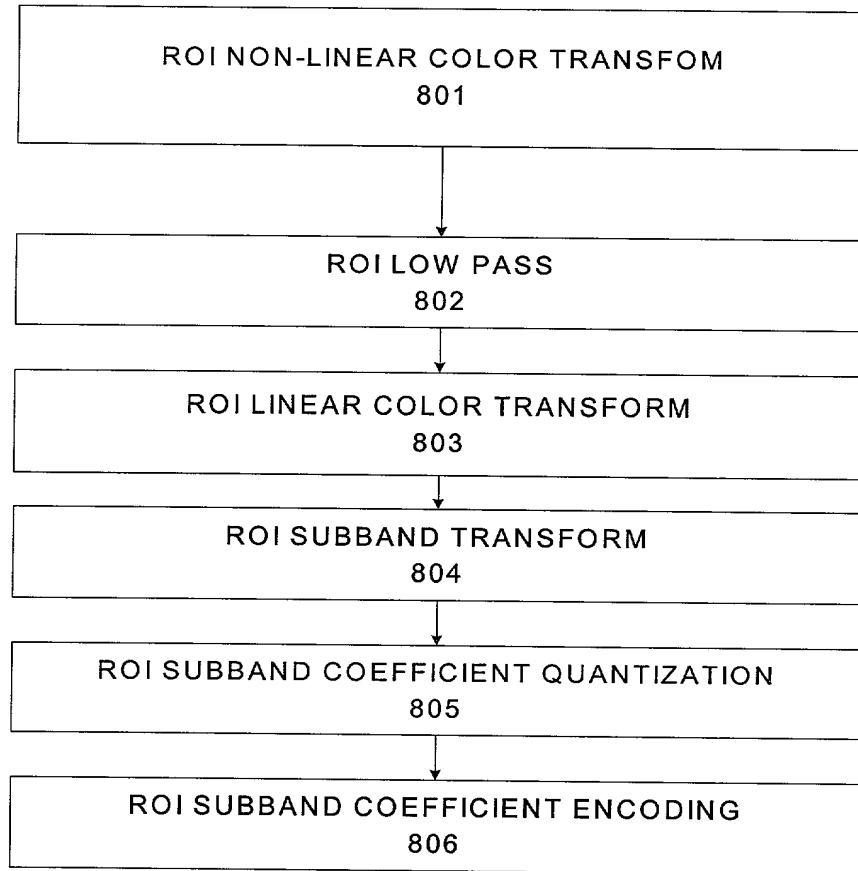


FIG. 8

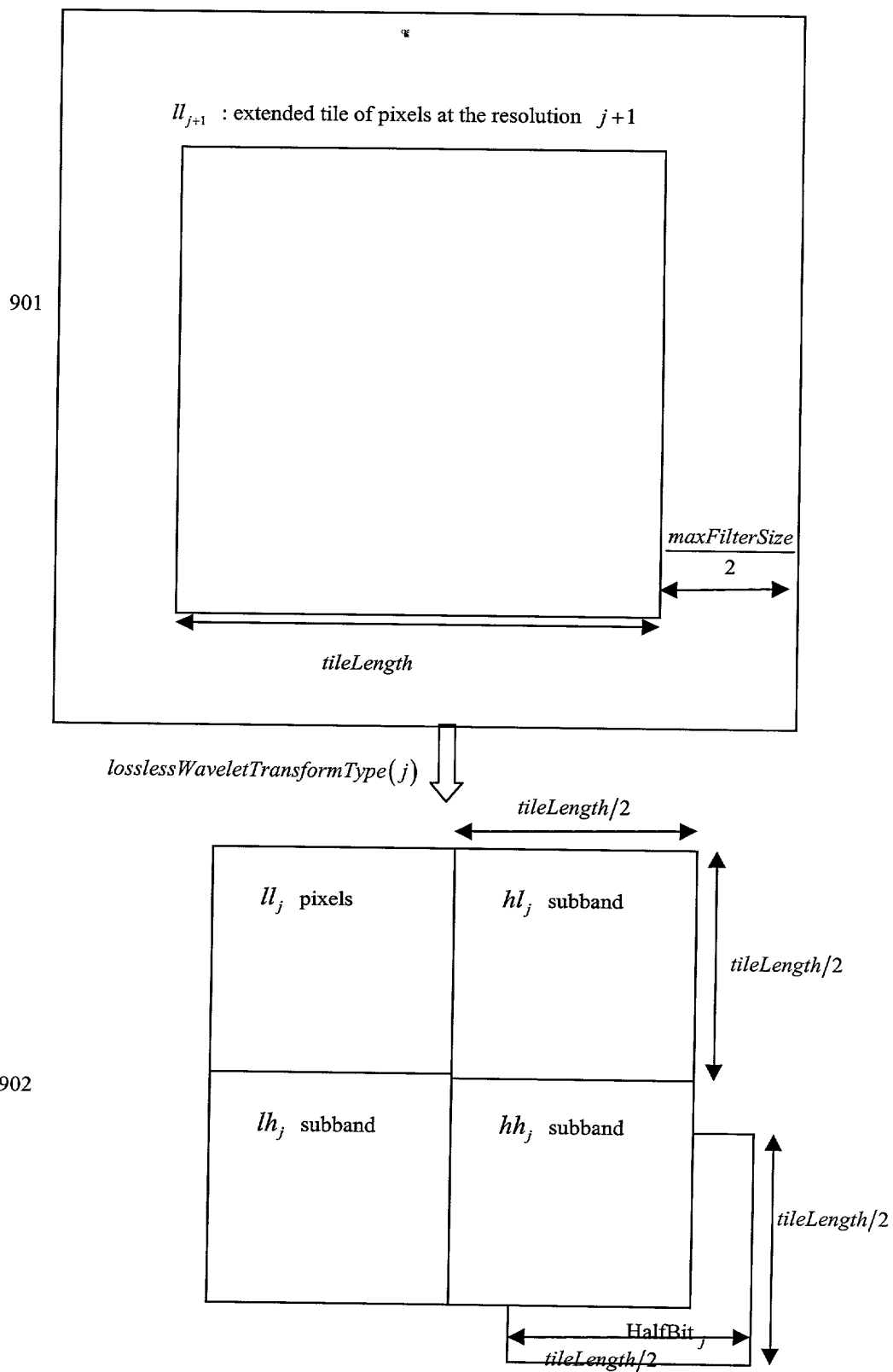
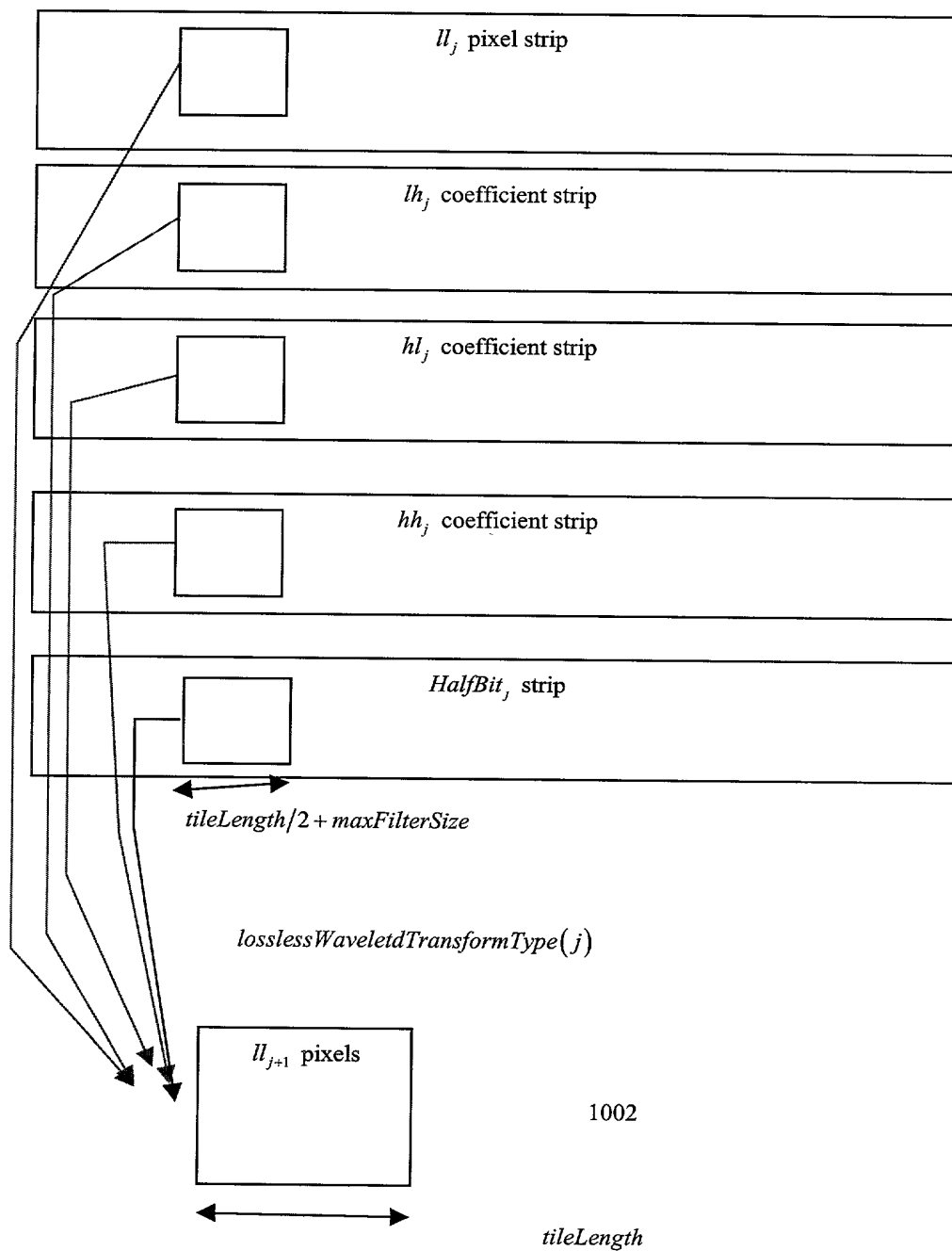


Figure 9

1001



1002

Figure 10

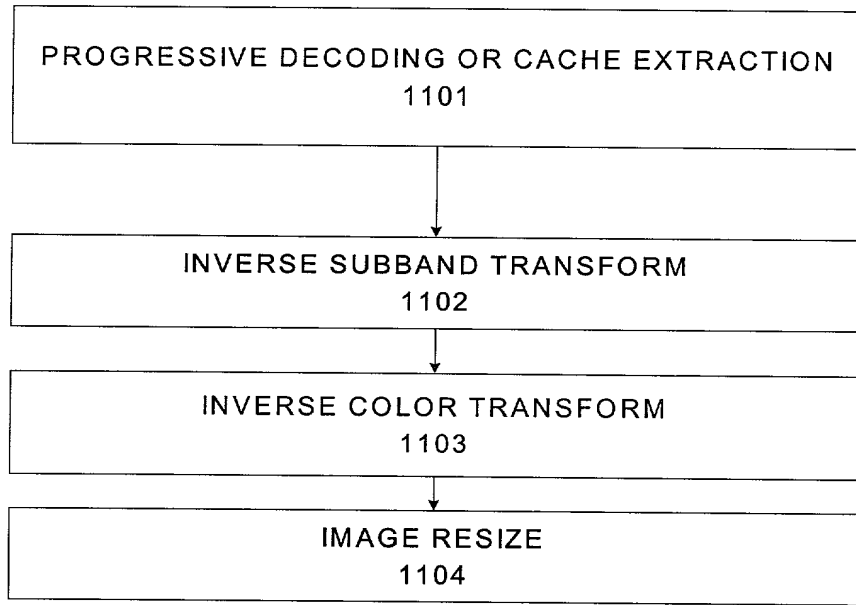
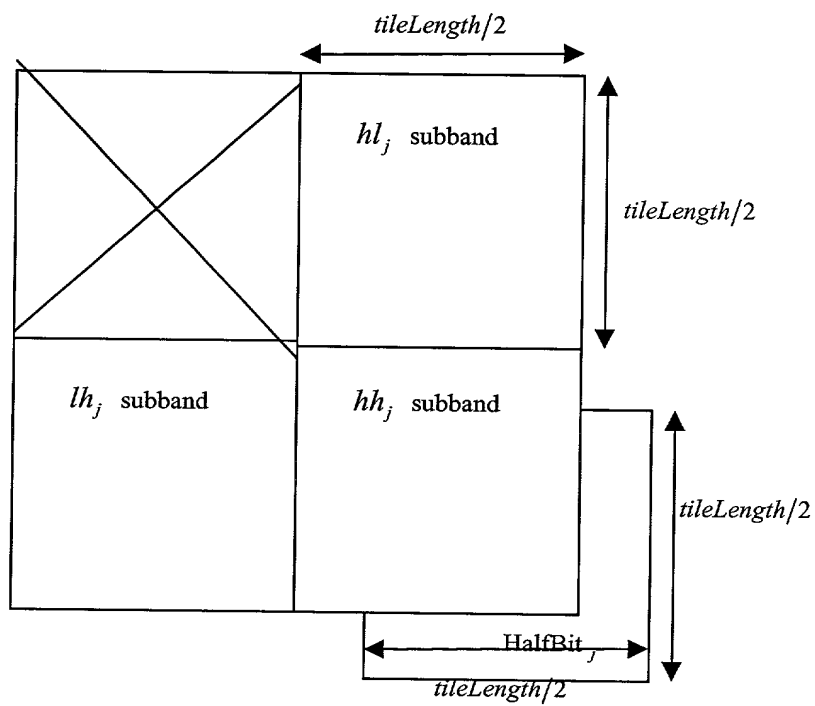


FIG. 11



1201

Figure 12

RGB <=> YUV Reversible Conversion

Forward:

$$Y_r = \left\lfloor \frac{R + 2G + B + 2}{4} \right\rfloor$$

$$U_r = R - G$$

$$V_r = B - G$$

Inverse:

1301

$$G = Y_r - \left\lfloor \frac{U_r + V_r + 2}{4} \right\rfloor$$

$$R = U_r + G$$

$$B = V_r + G$$

Figure 13

1401

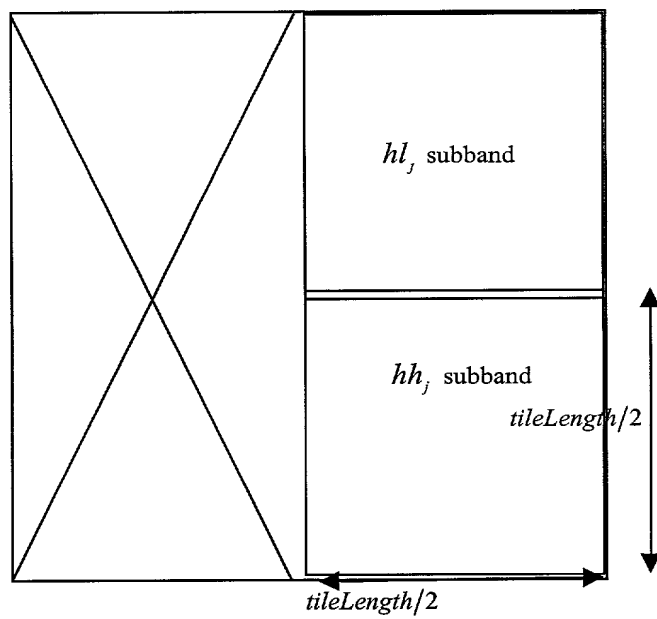


Figure 14

```

bitModel.startModel();
zeroCoefModel.startModel();
coefSignModel.startModel();

while (encoder.moreCoef()) {
    if (encoder.isCoefReported()) {

arithmetic_encode_symbol(bitModel,encoder.reportedCoefPrec
isionBit());
    }
    else {
        if ( encoder.isCoefExactZero() );
            arithmetic_encode_symbol(zeroCoefModel,true);
        else {
            arithmetic_encode_symbol(zeroCoefModel,false);
            arithmetic_encode_symbol(coefSignModel,encoder.getCoefSign());
        }
    }
}

```

(a)

```

bitModel.startModel();

for (int i = 0 ; i < hBlockSize ; i++) {
    for (int j = 0 ; j < hBlockSize ; j++) {
        arithmetic_encode_symbol(bitModel,
coefHalfBit[i][j]);
    }
}

```

(b)

Figure 15

```

        bitModel        .startModel();
zeroCoefModel.startModel();
coefSignModel.startModel();

decoder.initializeLSBPlaneCoefScan();

while (decoder.moreCoef()) {
    if (decoder.isCoefReported()) {
        if(decoder.isLHCoef()) {
            decoder. updateLSB (0);
        }
        else {
            decoder.updateLSB(arithmetic_decoder_symbol(bitModel));
        }
    }
    else {
        if(!decoder.isLHCoef()) {
            if (!arithmetic_decoder_symbol(zeroCoefModel))
                decoder.setLSB(arithmetic_decoder_symbol(coefSignModel));
        }
    }
}

```

(a)

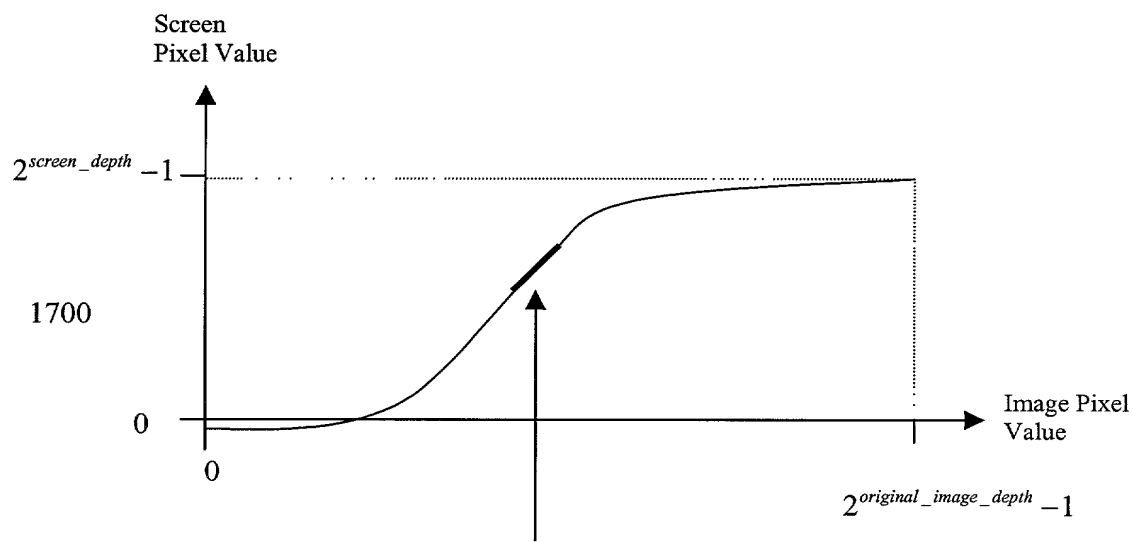
```

bitModel.startModel();
for (int i = 0 ; i < hBlockSize ; i++) {
    for (int j = hBlockSize ; j ; j--,p++) {
        coefHalfBit[i][j] = arithmetic_decoder_symbol(bitModel);
    }
}

```

(b)

Figure 16

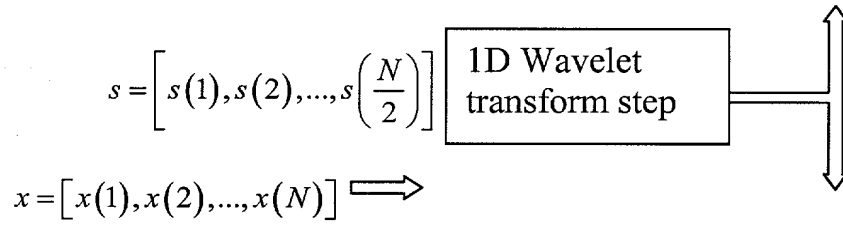


1701

Figure 17

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1800



$$d = \left[d(1), d(2), \dots, d\left(\frac{N}{2}\right) \right]$$

$$X = \begin{bmatrix} x(1,1) & x(1,2) & \cdots & x(1,N) \\ x(2,1) & x(2,2) & \cdots & x(2,N) \\ \vdots & \vdots & \ddots & \vdots \\ x(M,1) & x(M,2) & \cdots & x(M,N) \end{bmatrix}$$



1801

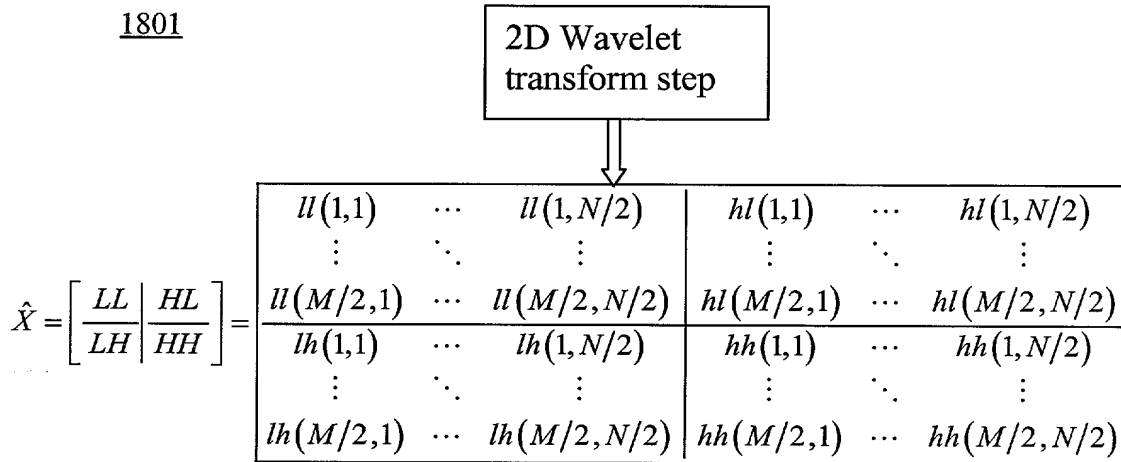
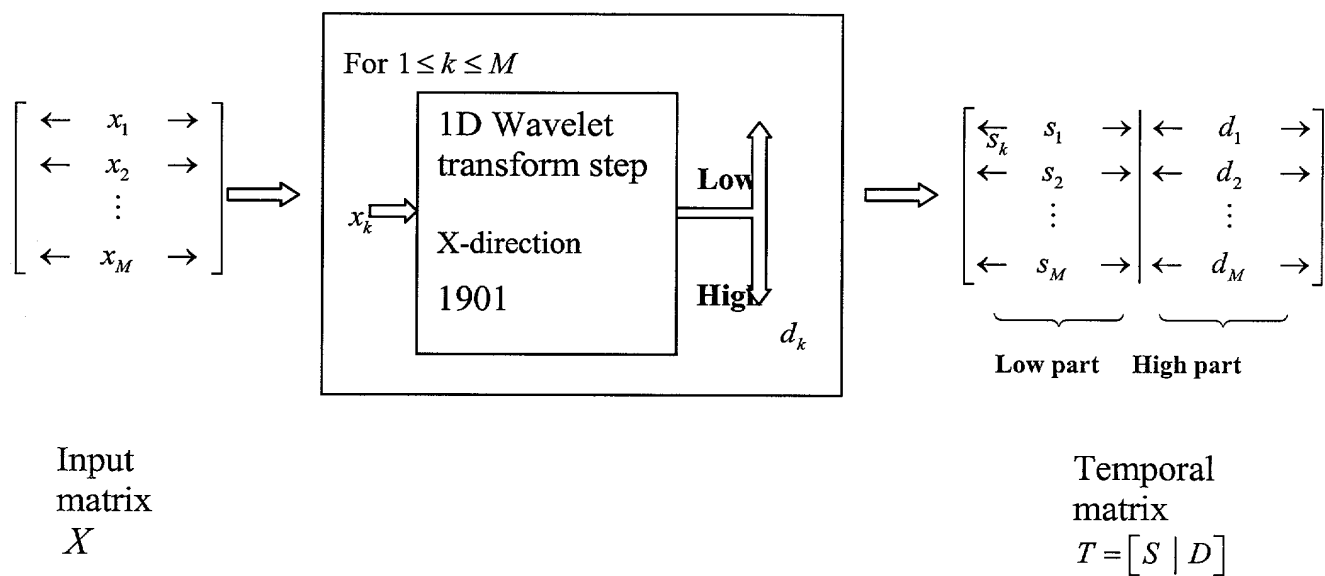


Figure 18



1901

Figure 19

$$\left[\begin{array}{cccc|cccc} \uparrow & \uparrow & \dots & \uparrow & \uparrow & \uparrow & \dots & \uparrow \\ s^1 & s^2 & \dots & s^{N/2} & d^1 & d^2 & \dots & d^{N/2} \\ \downarrow & \downarrow & \dots & \downarrow & \downarrow & \downarrow & \dots & \downarrow \end{array} \right] \quad 2000$$

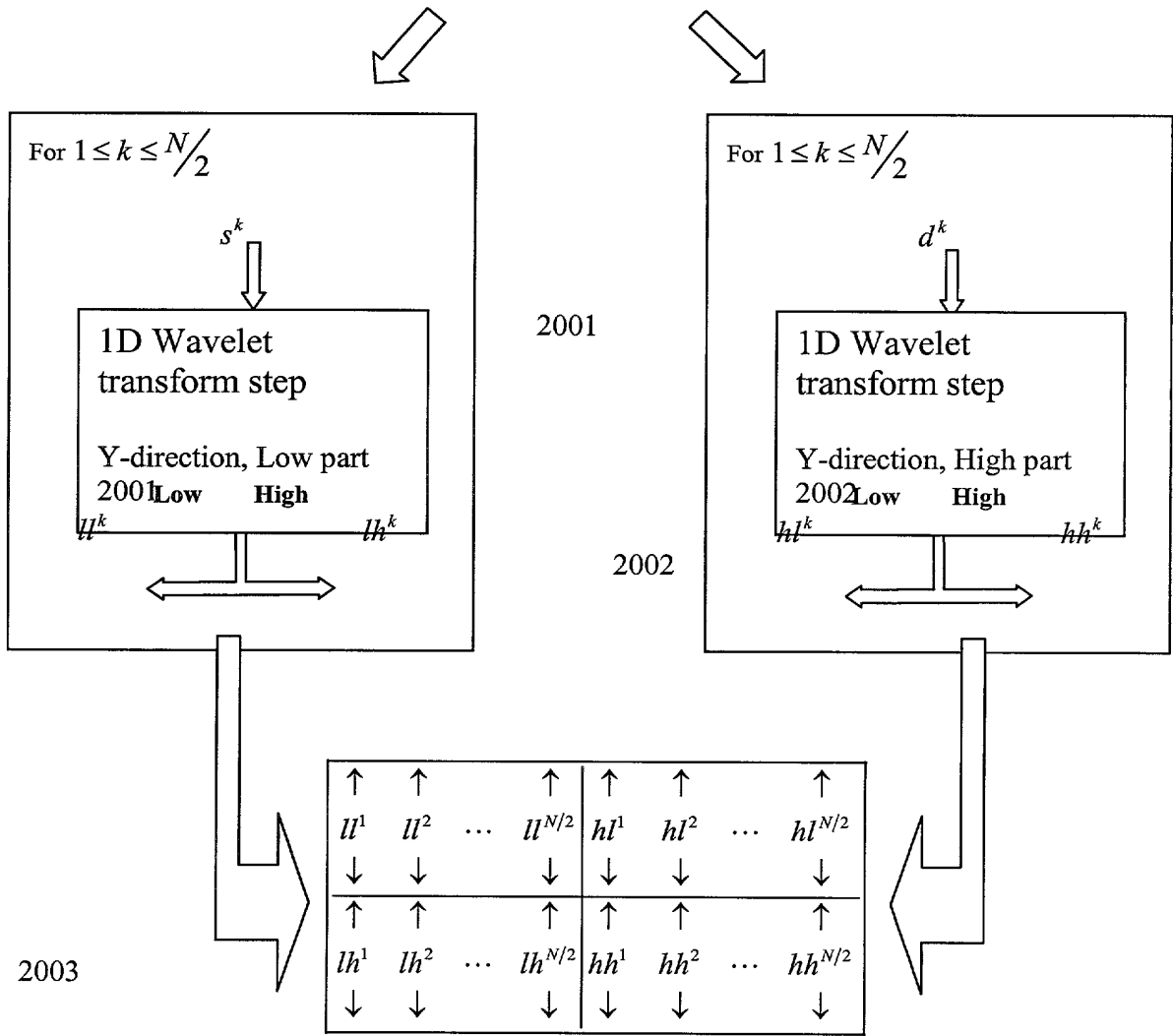


Figure 20

Let I be the original Image,

$$X_0 = I \quad \longrightarrow \quad \boxed{\begin{array}{c} \text{2D Wavelet} \\ \text{transform step} \end{array}} \quad \longrightarrow \quad \left[\begin{array}{c|c} LL_0 & HL_0 \\ \hline LH_0 & HH_0 \end{array} \right]$$

For $0 < i < Levels$

$$X_i = LL_{i-1} \quad \longrightarrow \quad \boxed{\begin{array}{c} \text{2D Wavelet} \\ \text{transform step} \end{array}} \quad \longrightarrow \quad \left[\begin{array}{c|c} LL_i & HL_i \\ \hline LH_i & HH_i \end{array} \right]$$

2100

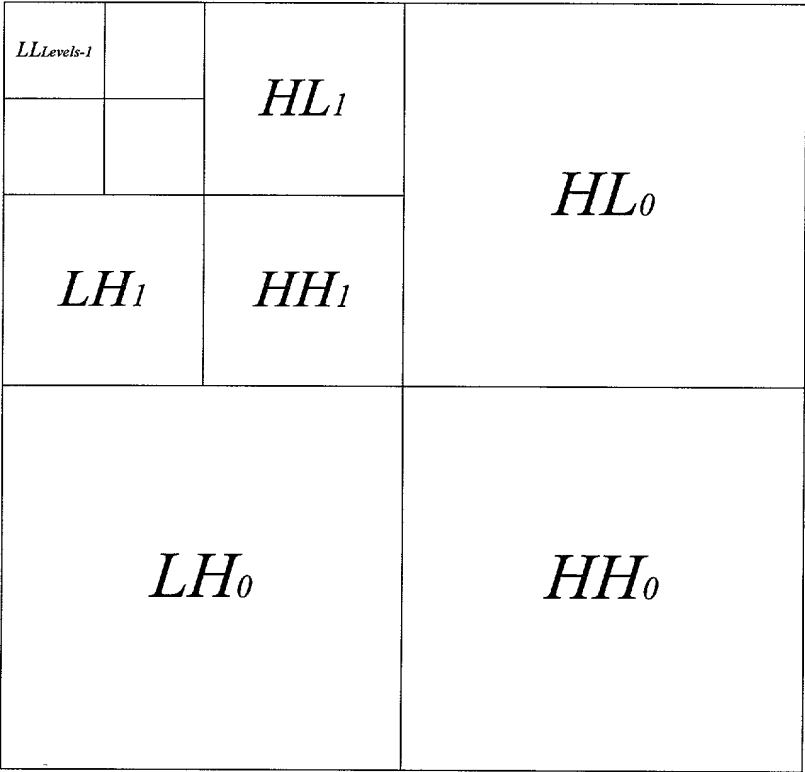


Figure 21

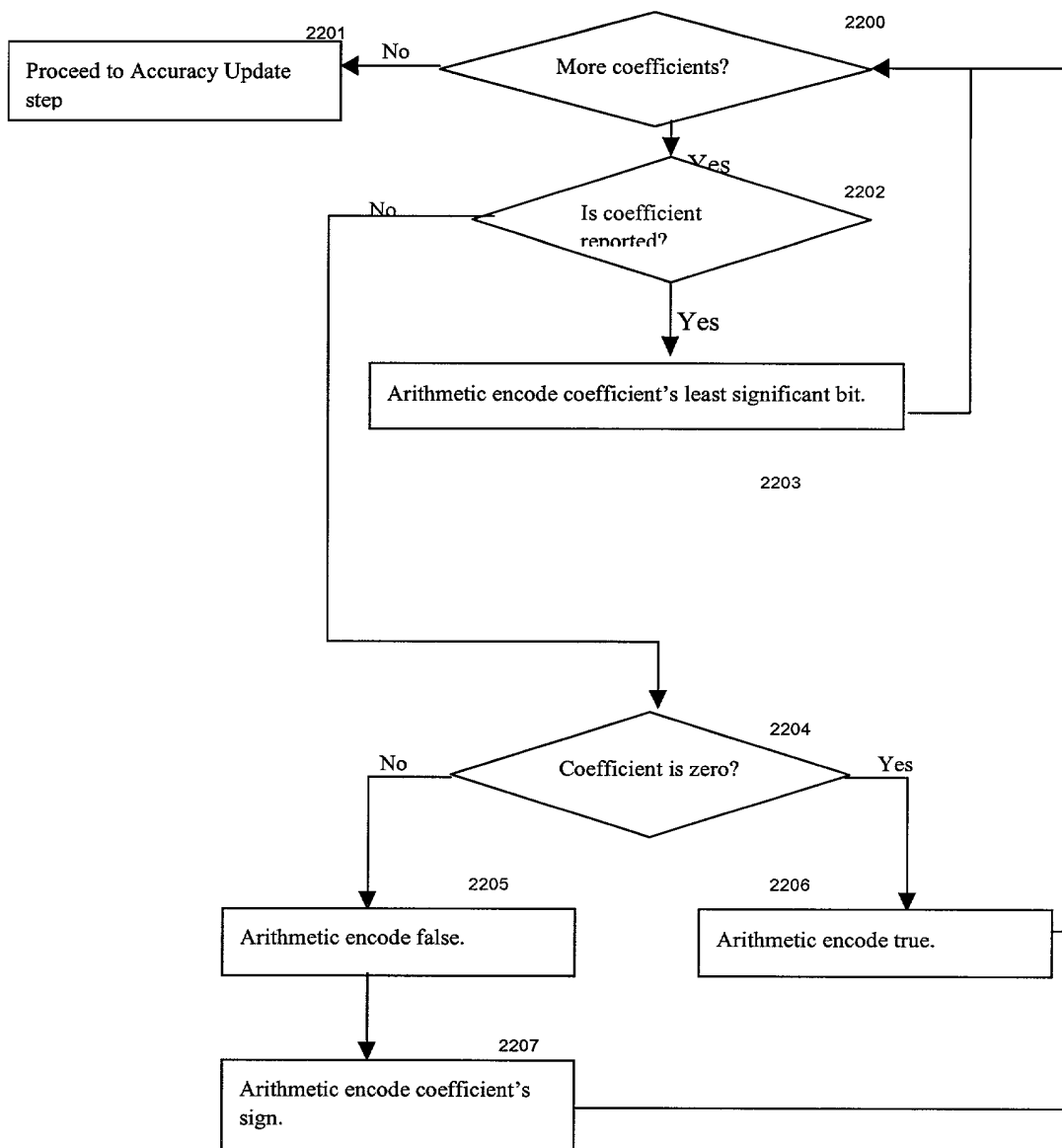


Figure 22

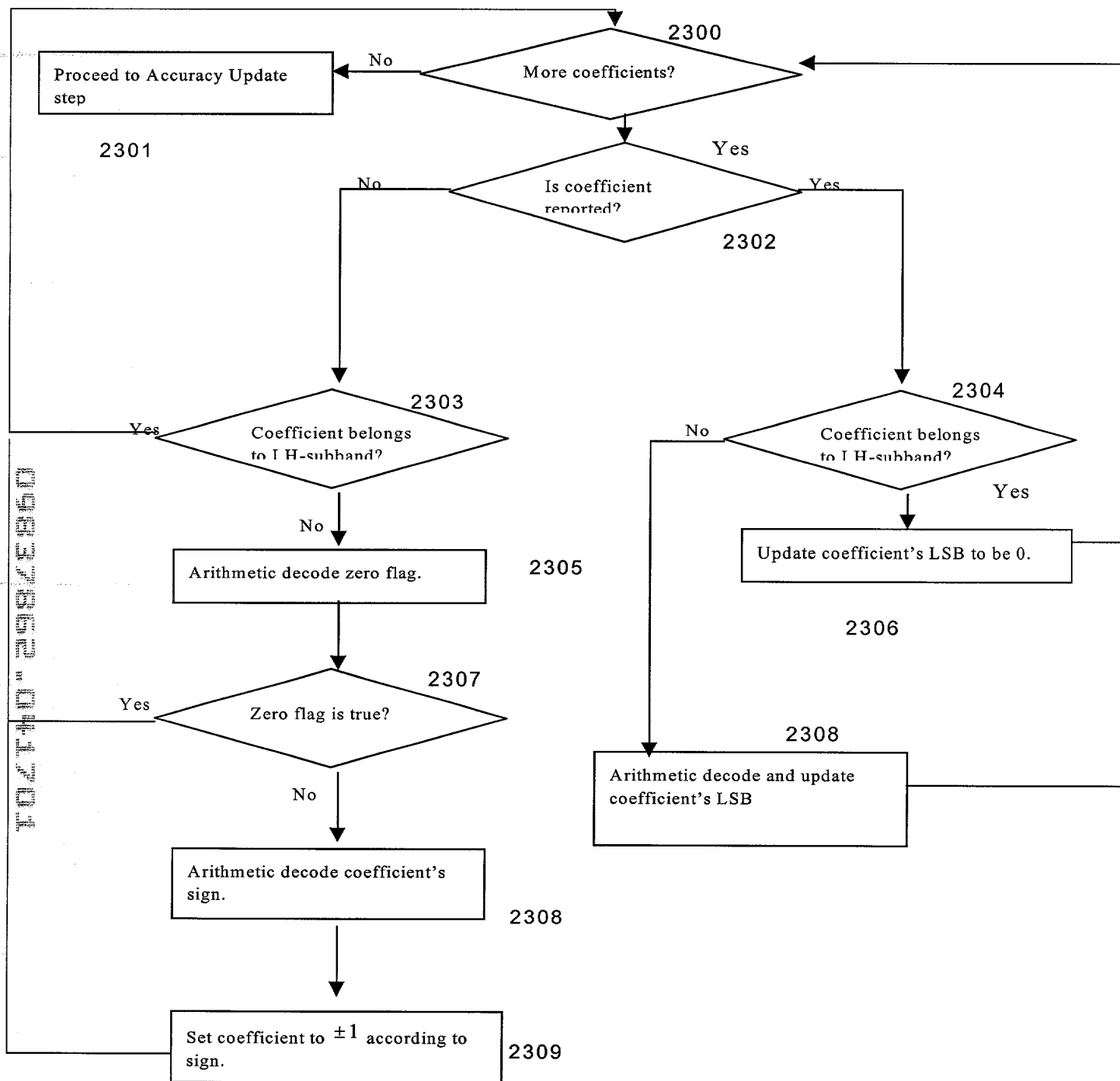


Figure 23

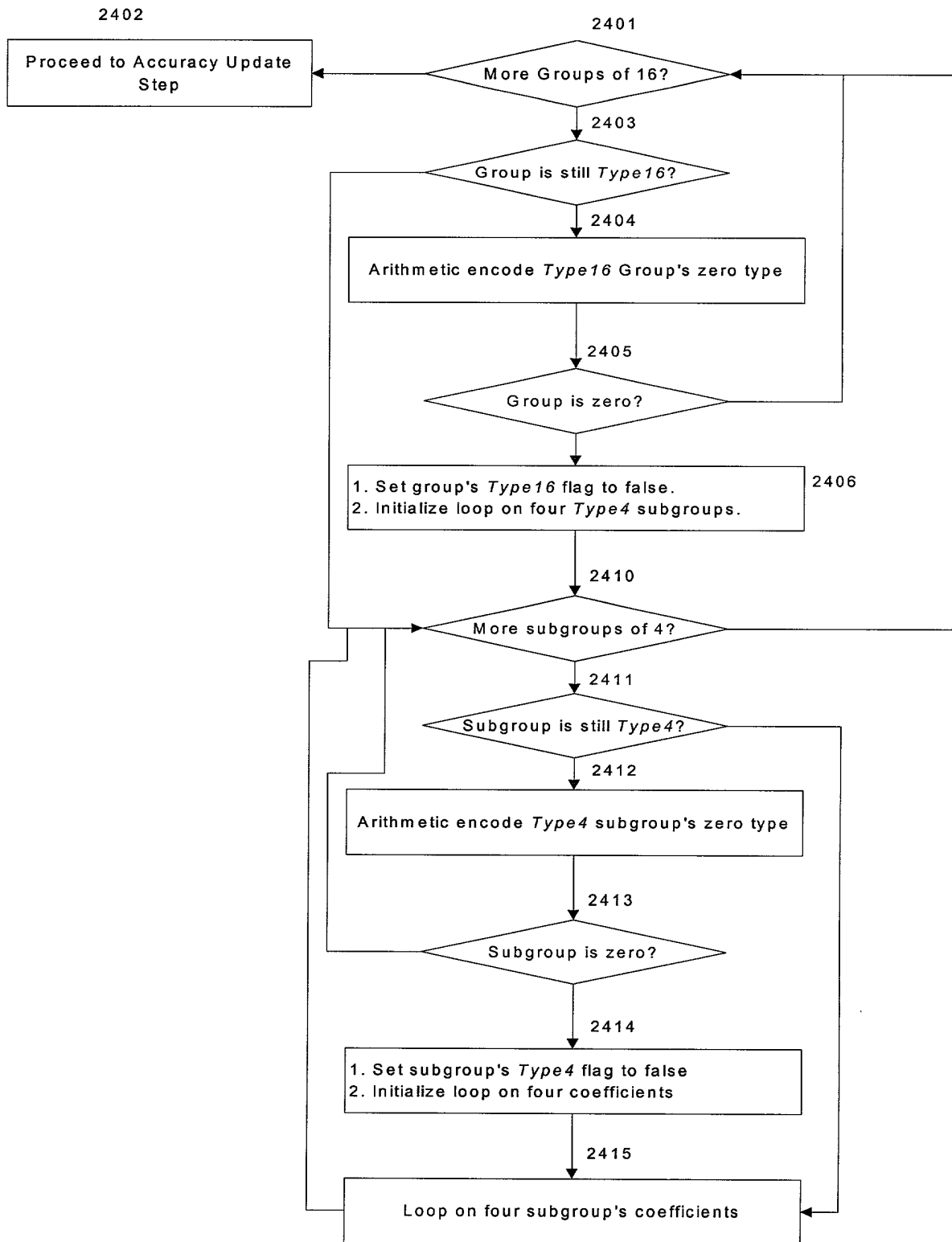


FIG. 24

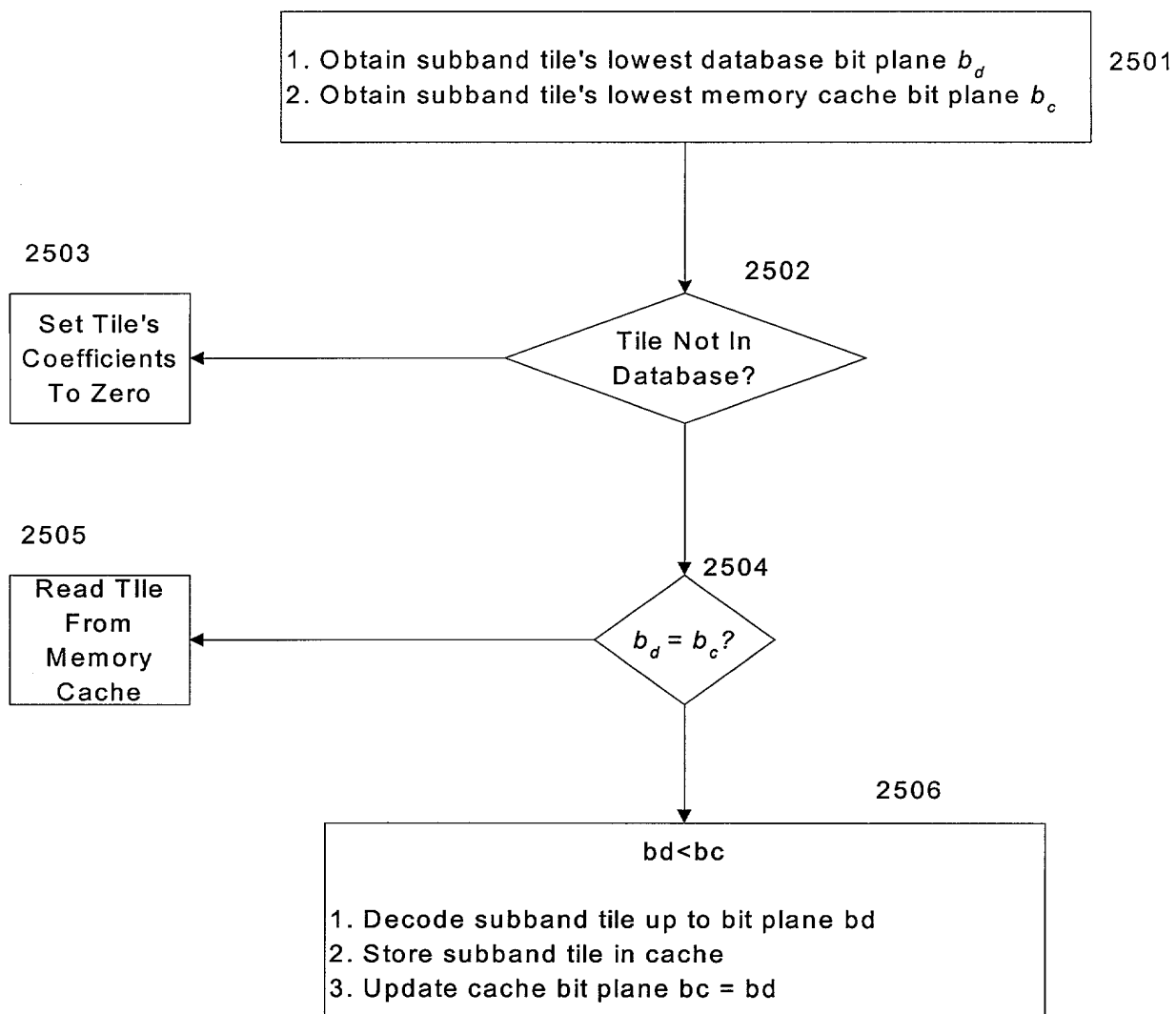


Fig. 25

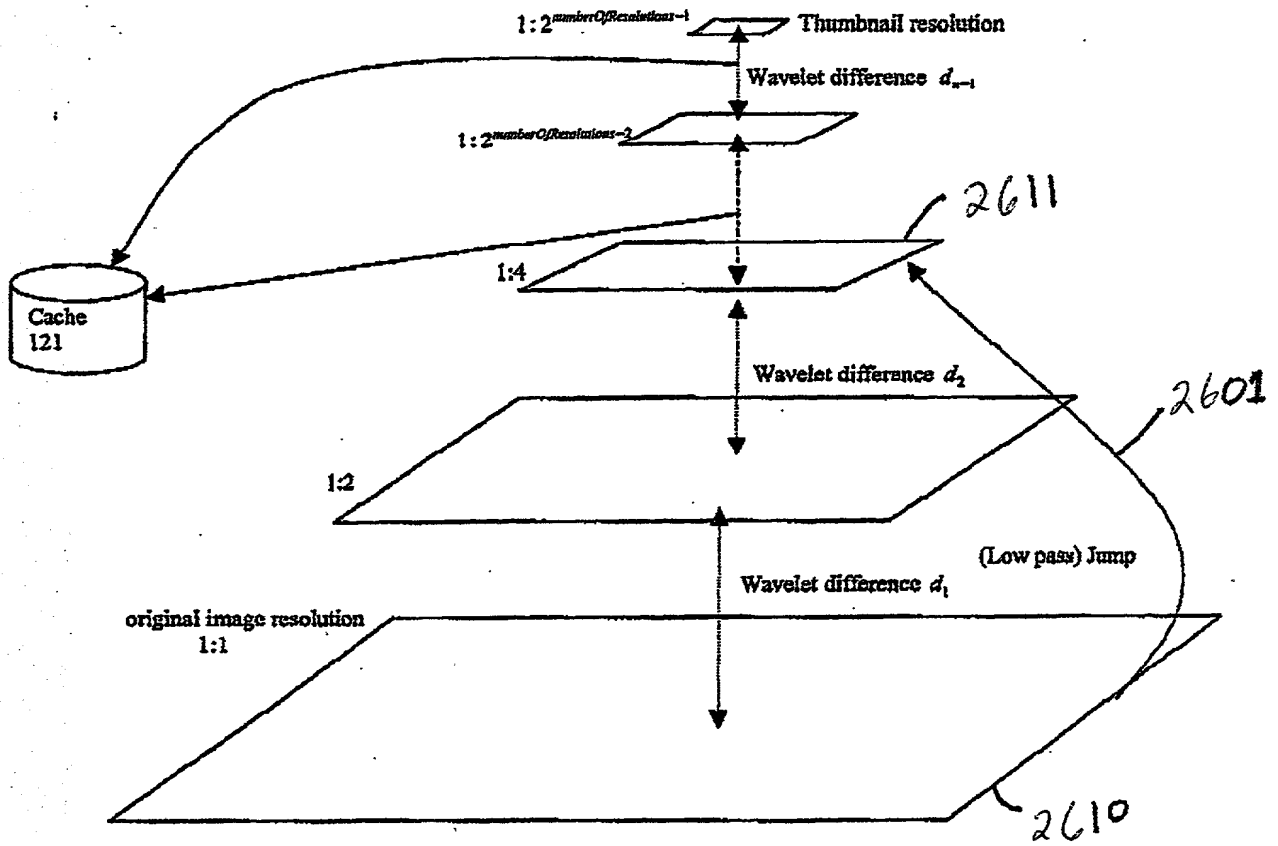


Figure 26 Preprocessing multiresolution structure